

FIG. 1A

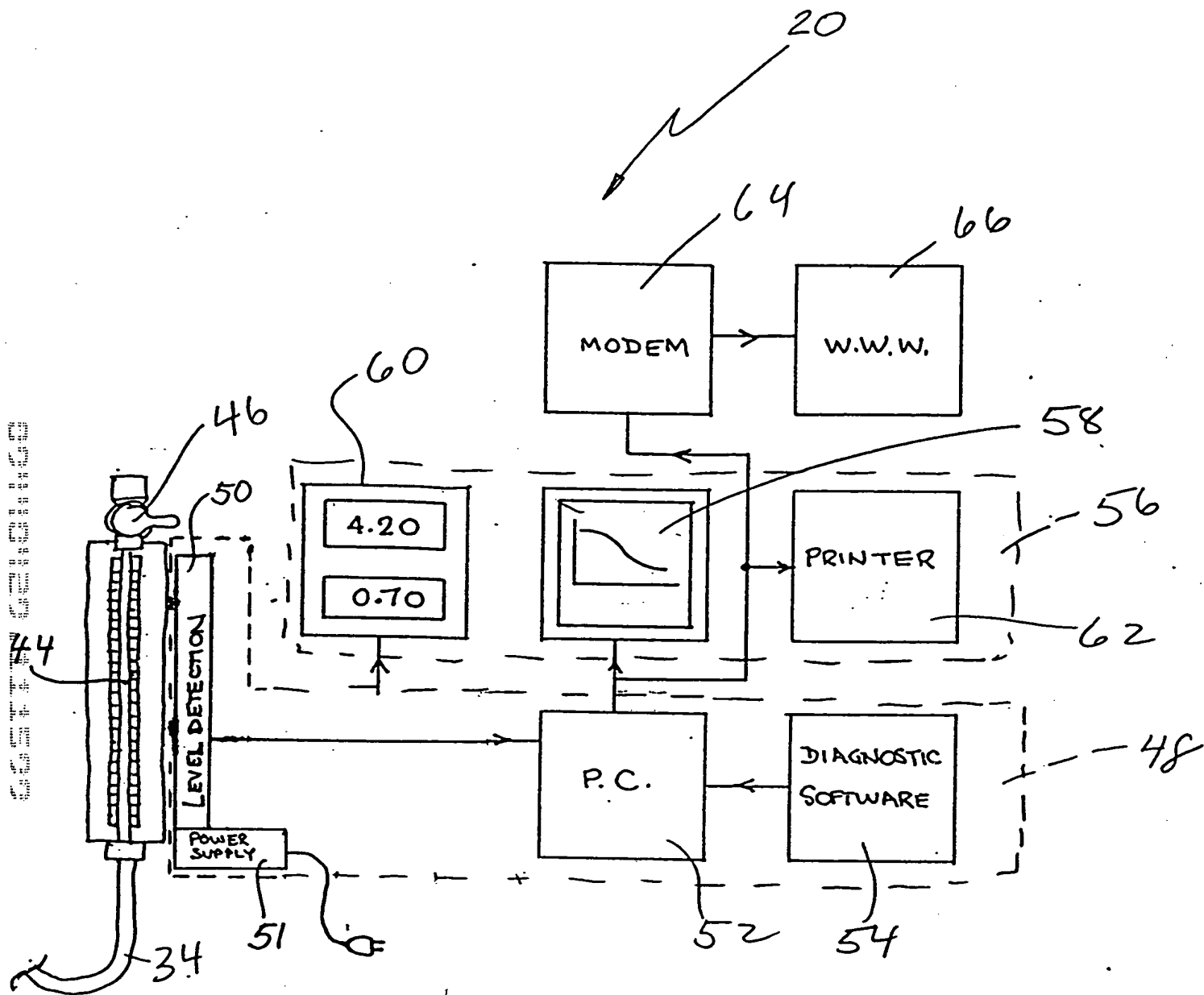


FIG. 1B 24

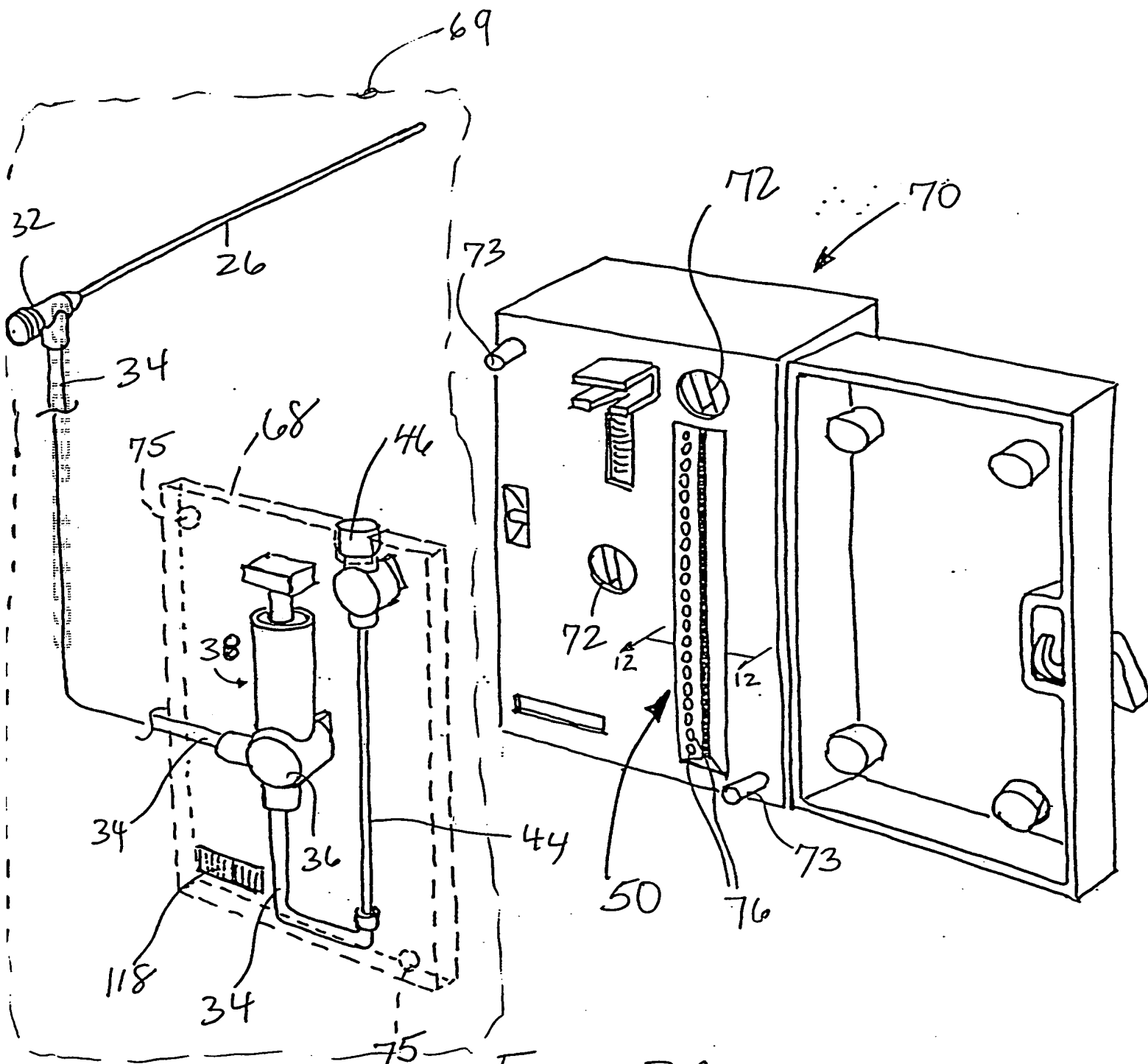


FIG. 2A

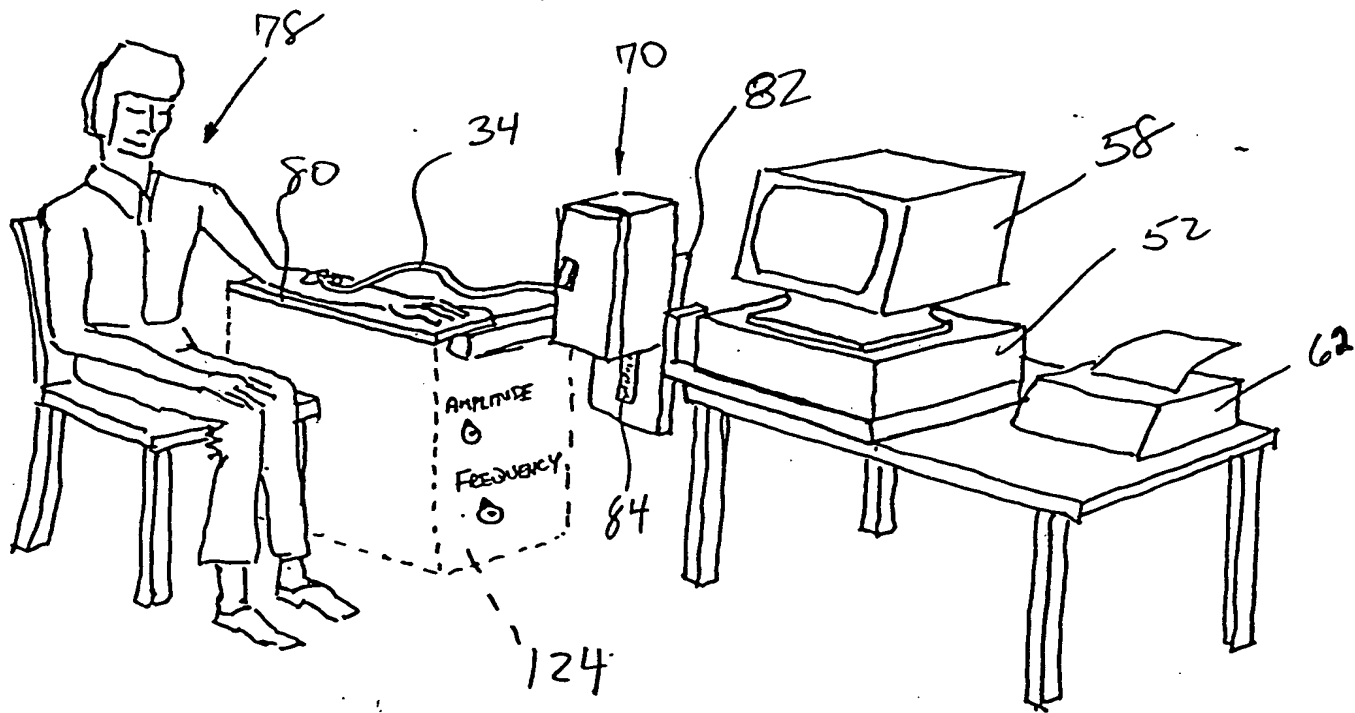


FIG. 2B

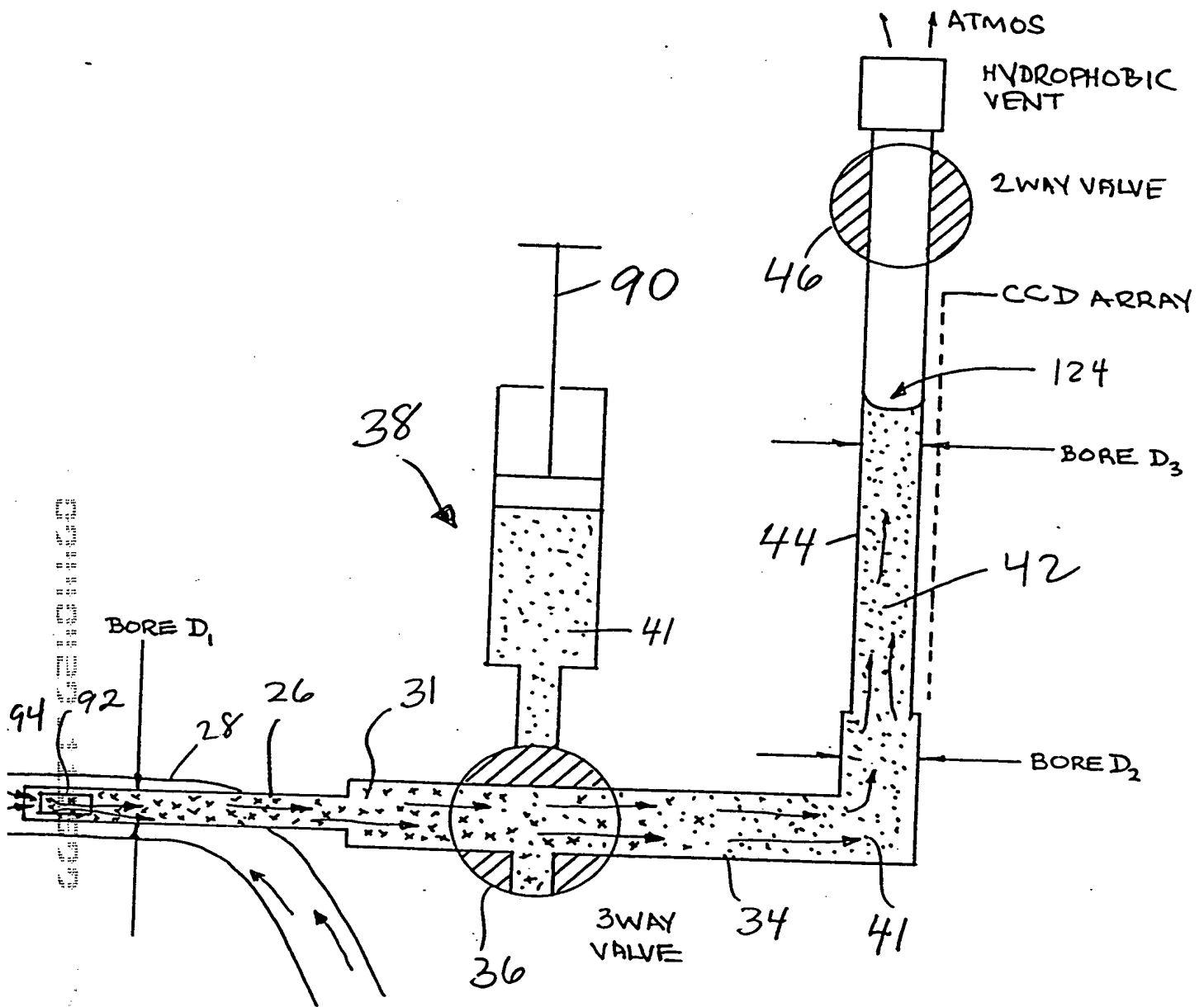
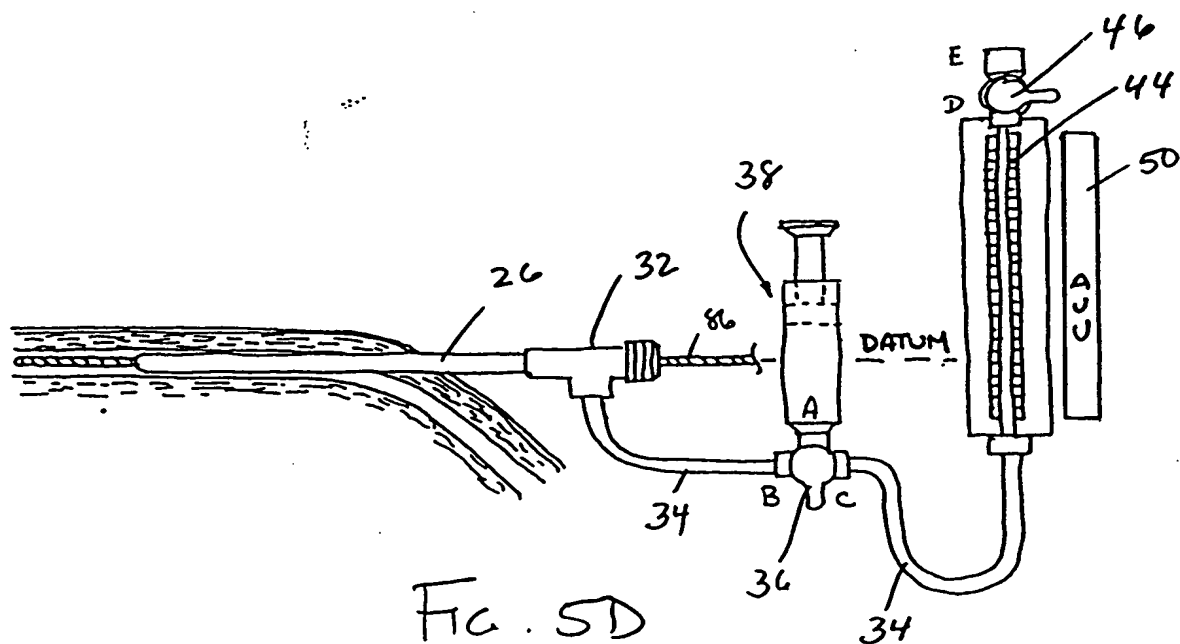
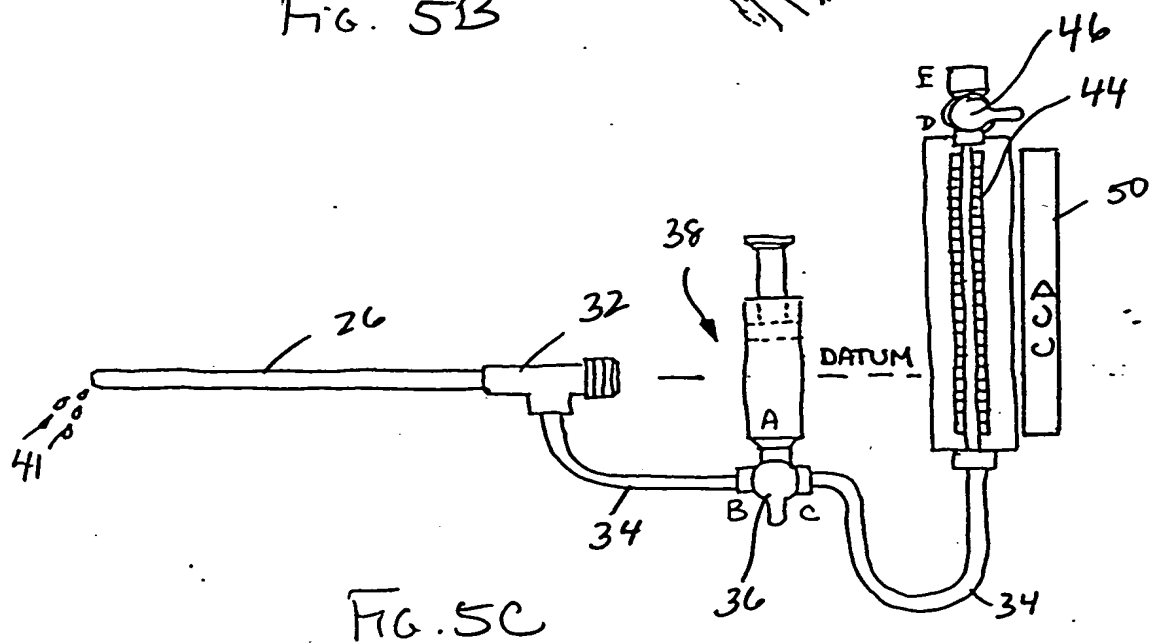
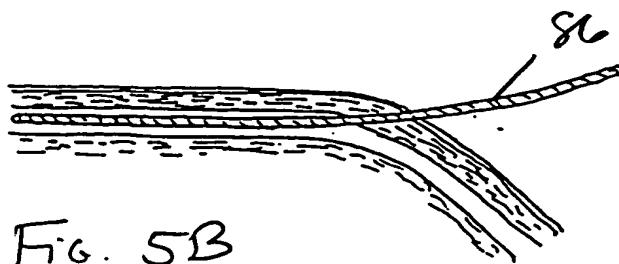
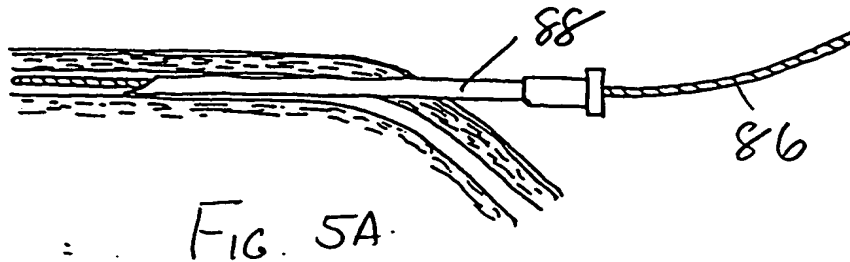
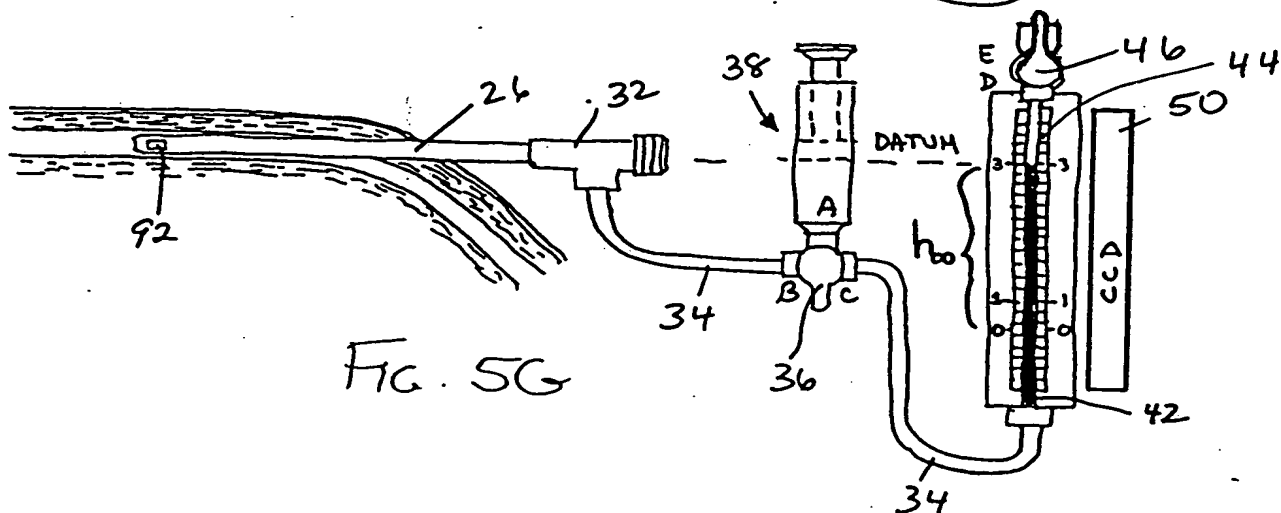
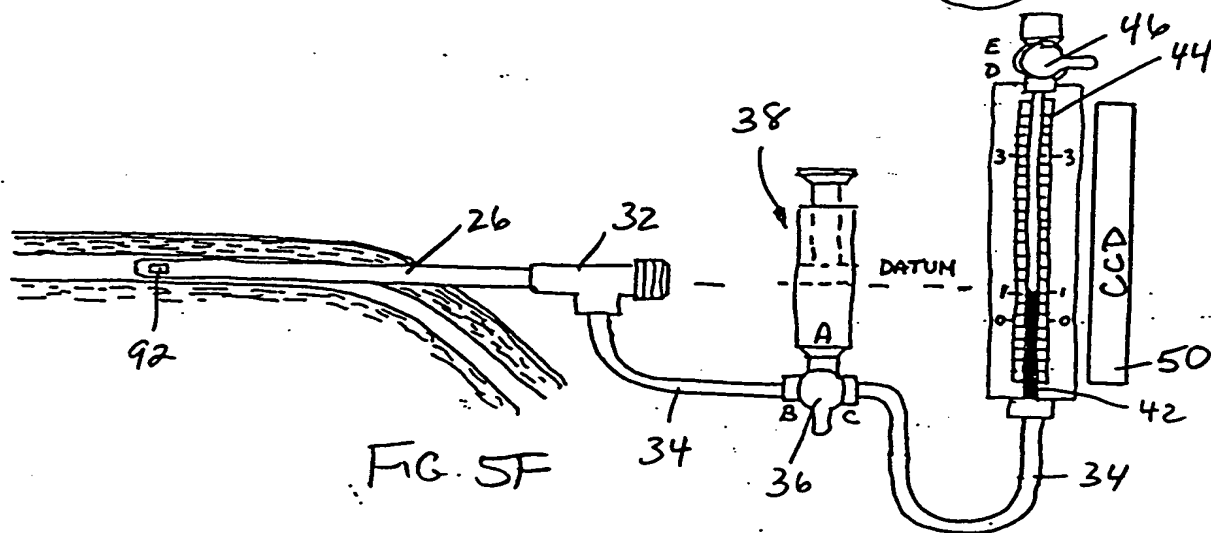
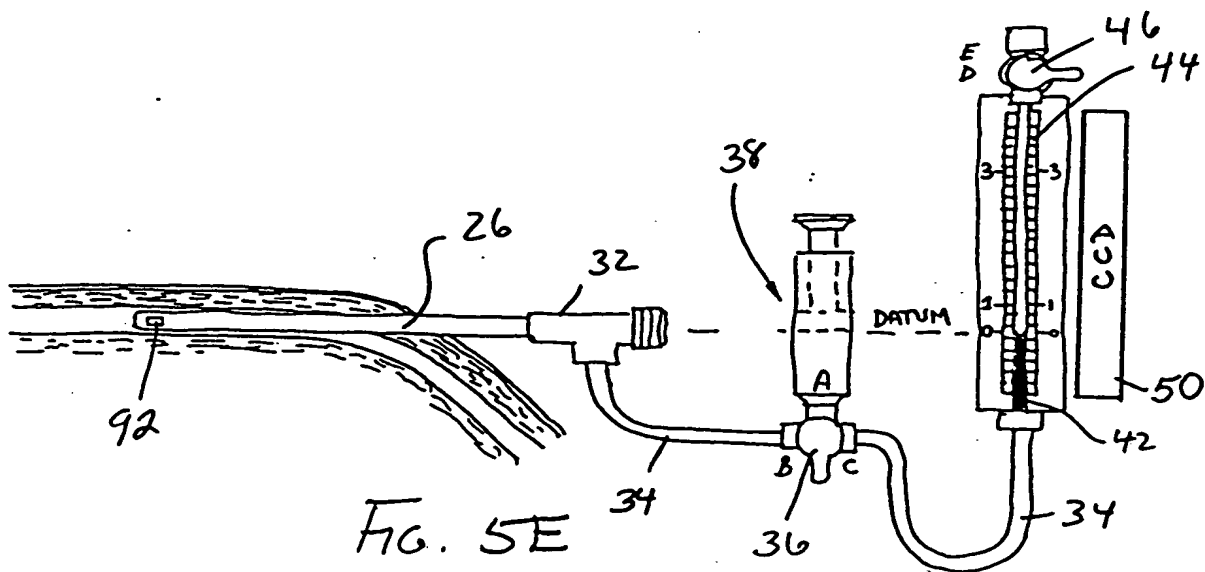
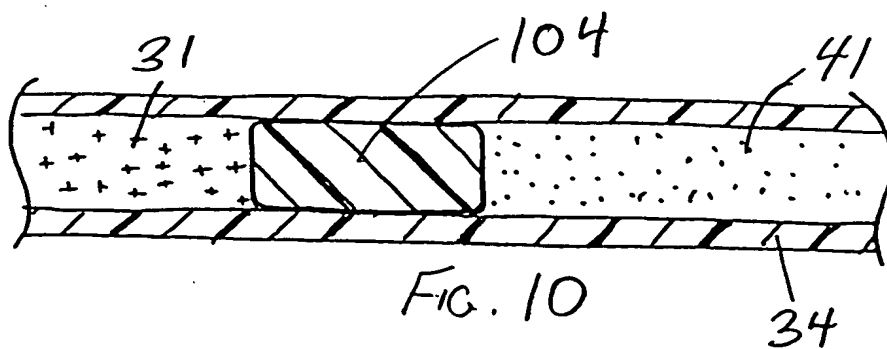
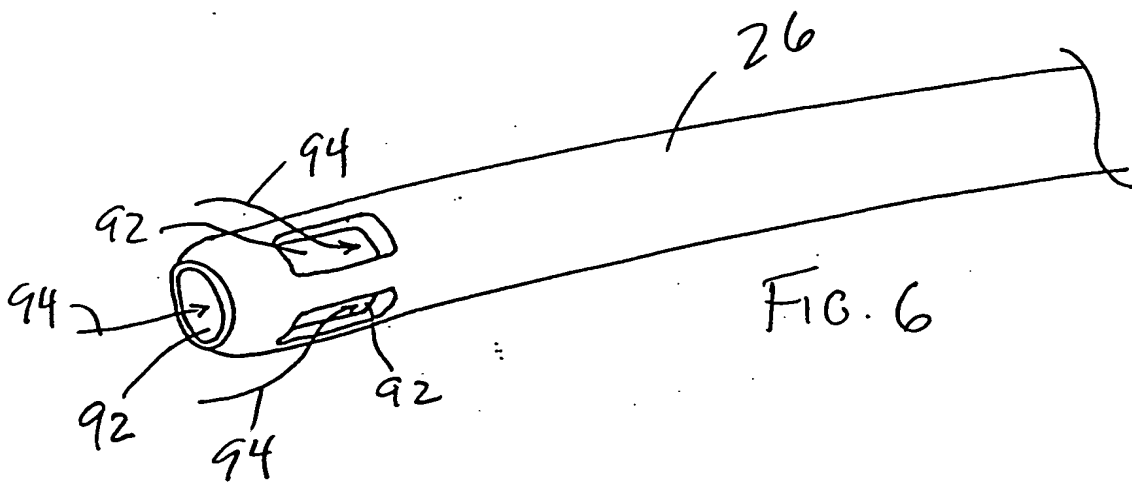
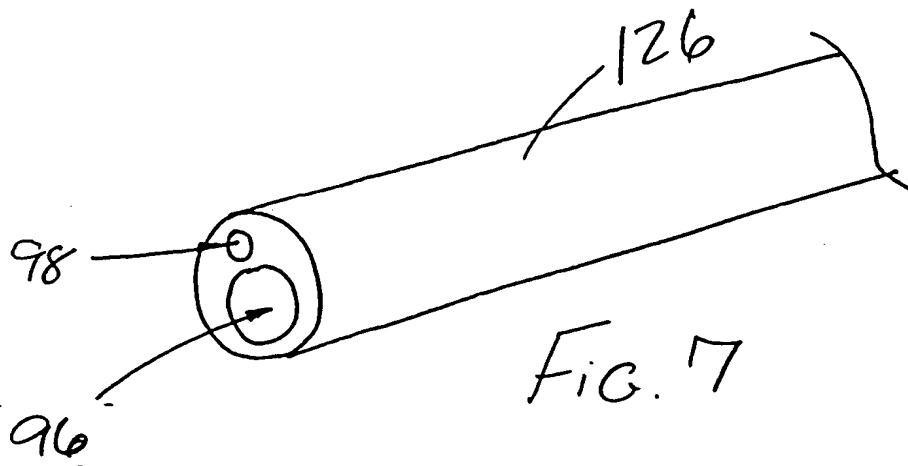


FIG. 3









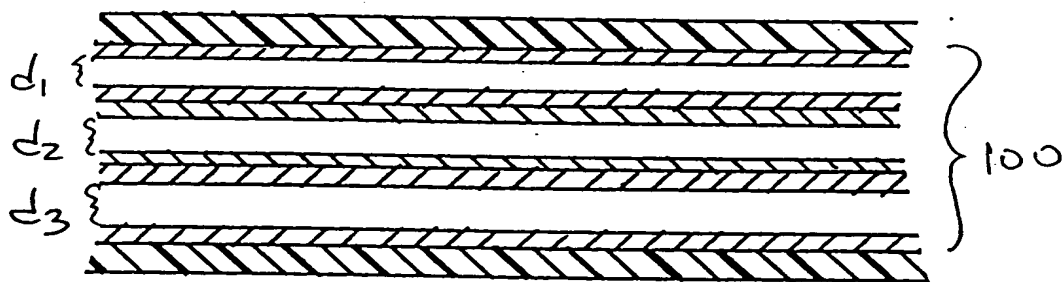
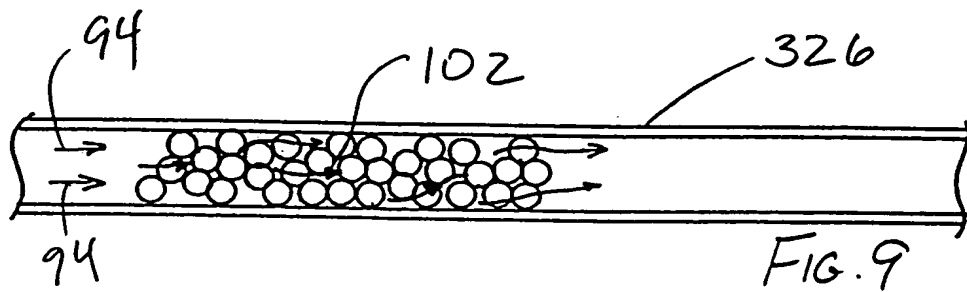


FIG. 8B

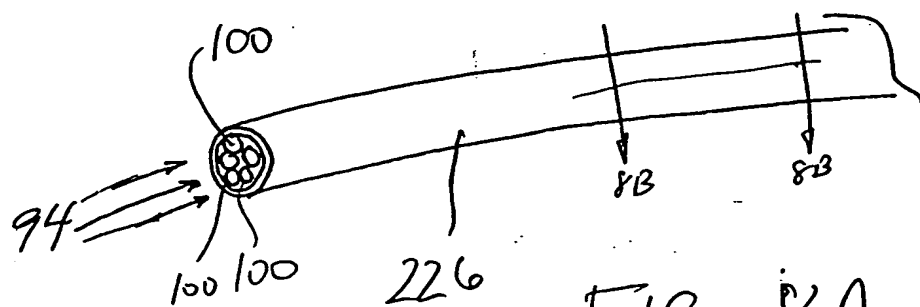


FIG. 8A

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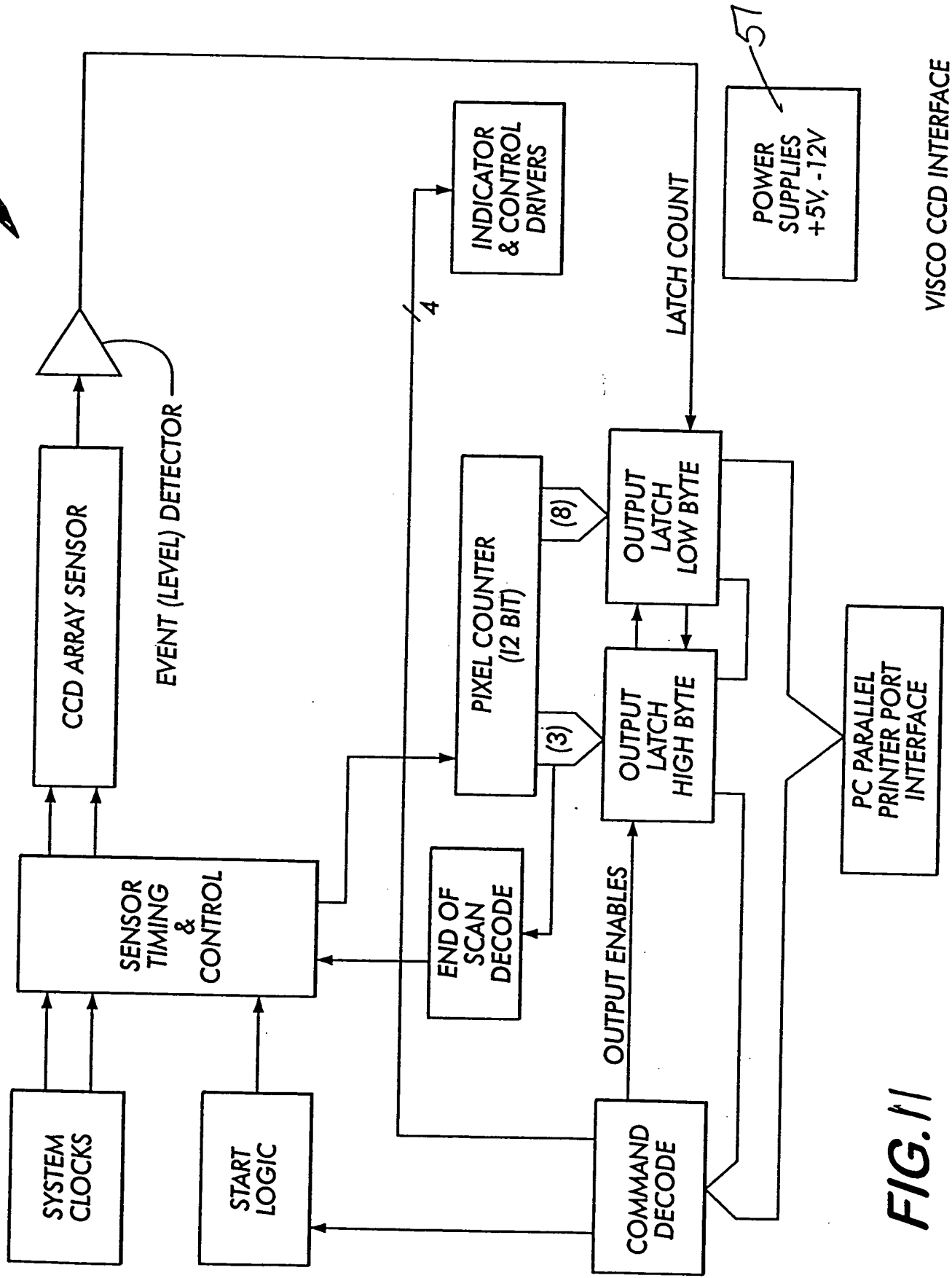


FIG. 11

VISCO CCD INTERFACE

FIG. 11 is a block diagram of a VISCO CCD INTERFACE system.

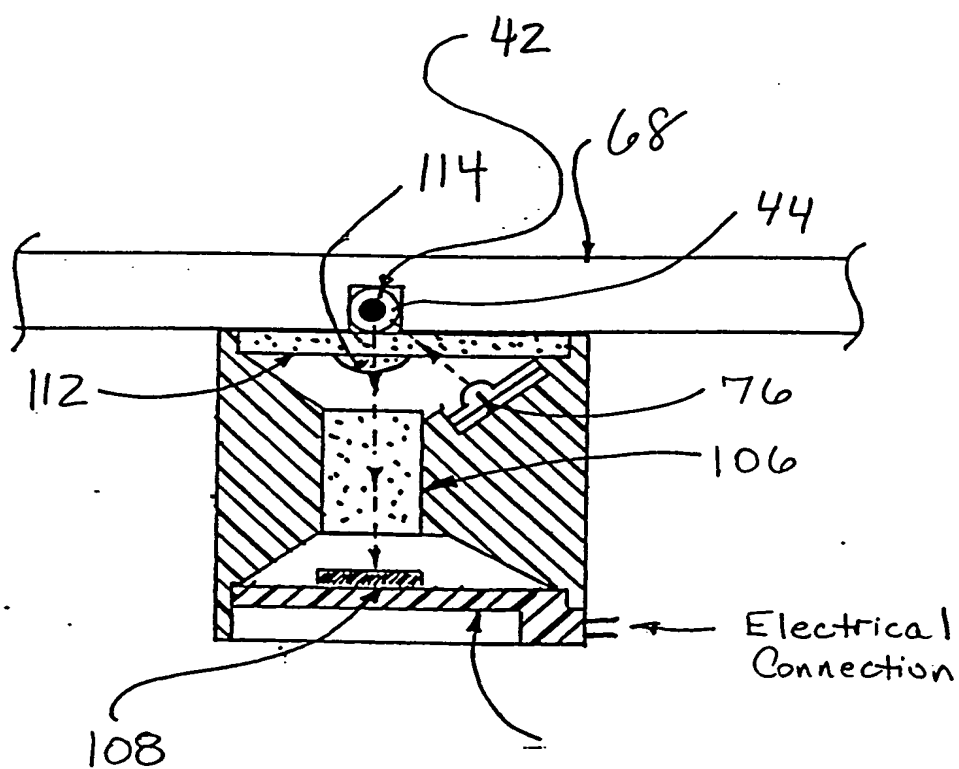


FIG. 12

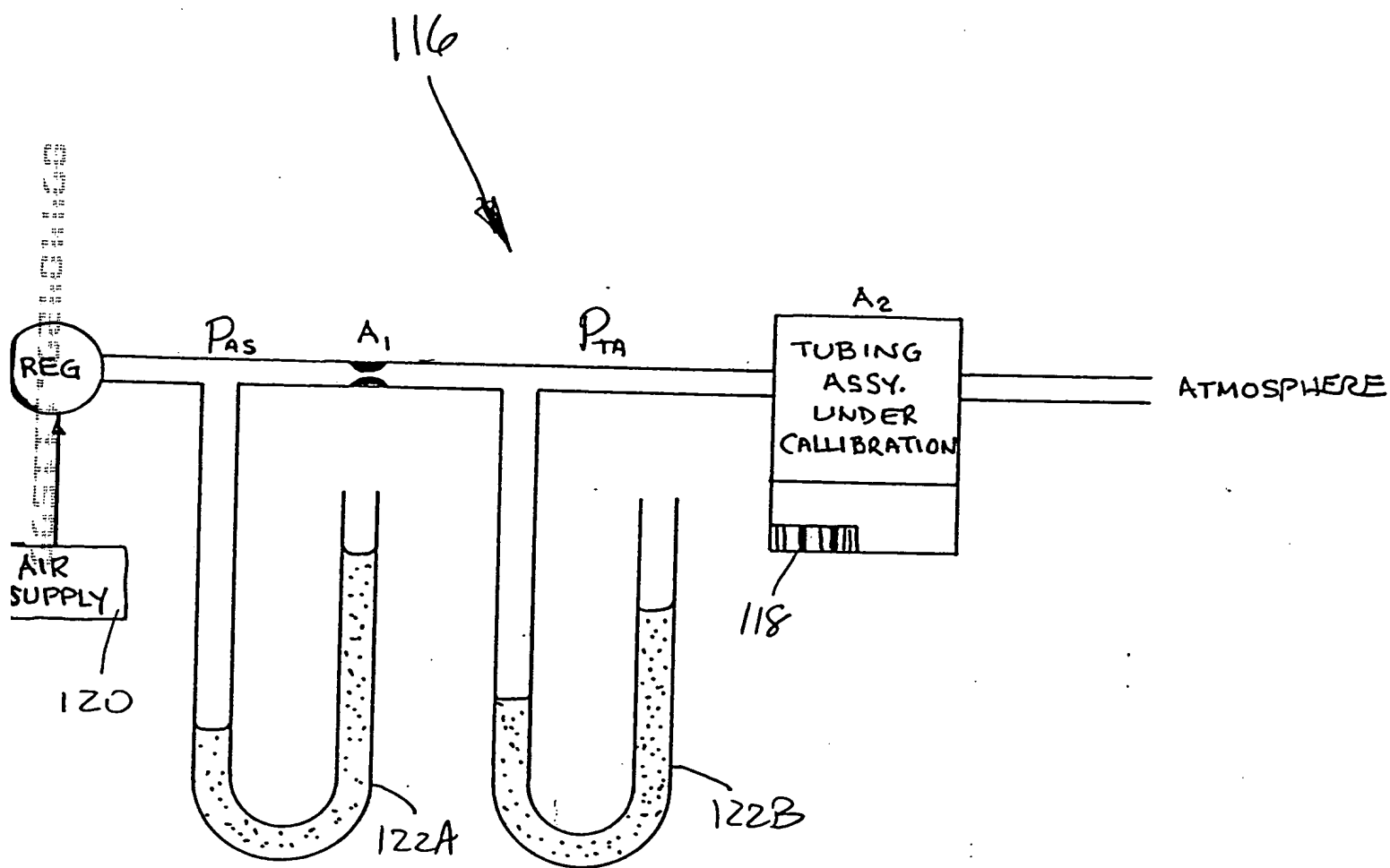


FIG. 13

